

MEMORY CONTROLLERS HAVING
PINS WITH SELECTABLE FUNCTIONALITY

Abstract of the Disclosure

Systems and methods for selecting pin
5 functionality in memory controllers are provided.
These memory controllers have pins that can be used to
drive different types of signals, depending on the type
of memory coupled to the memory controller. For
example, pins can be used to drive clock signals or
10 chip select signals. Accordingly, because different
types of memory require different numbers of clock and
chip select signals, the same memory controller can be
advantageously used with different types of memory.
Moreover, memory controller pins that would ordinarily
15 go unused with some types of memory can now be used to
increase the number of such memories that can be
coupled to the memory controller.